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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

BOX: Office of Initial Patent Examination

Express Mail Receipt No.

Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Examiner Name	Unknown
Group Art Unit	2133
Issue Fee Batch No.	
Attorney Docket Number	X-1069

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ENCLOSURES *(check all that apply)*

- [ ] Fee Transmittal Form
- [ ] Amendment / Response
  - [ ] Preliminary Amendment
  - [ ] After Final
  - [ ] Affidavit(s)/declaration(s)
- [ ] Extension of Time Request
- [ ] Change Status to LARGE ENTITY
- [ ] Express Abandonment Request
- [ ] Information Disclosure Statement
- [ ] Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A)
- [ ] Certified Copy of Priority Document(s)
- [ ] Response to Missing Parts/ Incomplete Application
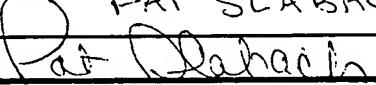 - [ ] Response to Missing Parts under 37 CFR 1.52 or 1.53

- [ ] Assignment Papers (with Recordation Cover Sheet)
- [ ] Declaration / Oath
- [ ] Drawing(s)
- [ ] Licensing-related Papers
- [ ] Petition -
- [ ] To Convert a Provisional Application
- [ ] Power of Attorney, Revocation Change of Correspondence Address
- [ ] Terminal Disclaimer
- [ ] Request for Refund

Remarks

- [ ] After Allowance Communication to
- [ ] Appeal Communication to Board of Appeals and Interferences
- [ ] Appeal Communication to Group *(Appeal Notice, Brief, Reply Brief)*
- [ ] Status Letter
- [X] Return Receipt Postcard
- [X] Additional Enclosure(s) (please identify below):

  Request for Corrected Filing Receipt; Supplemental Application Data Sheet

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number Attn:	H. C. Chan	**24309** *(Customer Number)*	Reg. Number 35,477
Signature			
Date	June 24, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number:	24-0040
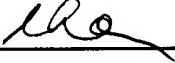
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Signature	Pat Slaback	Date	June 24, 2002

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IN THE UNITED STATES PATENT OFFICE

Applicant: David P. Schultz

Assignee: Xilinx, Inc.

Title: "Method and System for Flexibly JTAG TAP Controllers for FPGA-Based System-On-Chip (SoC)"

Serial No.: 10/086,129   File Date: 02-28-02

Examiner: Unknown   Art Unit: 2133

Docket No.: **X-1069 US**

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<u>REQUEST FOR CORRECTED FILING RECEIPT</u>

Dear Sir:

Applicant requests a corrected Filing Receipt in the above identified patent application, which includes the following changes:

Please correct the title of the patent application to read: "Method and System for Flexibly Nesting JTAG TAP Controllers for FPGA-Based System-On-Chip (SoC)".

A red-lined copy of the Filing Receipt is enclosed for clarification. Also enclosed is a Supplemental Application Data Sheet which also corrects the title.

No fee is due with this communication.

The Commissioner is hereby authorized to charge any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

H. C. Chan
Attorney for Applicant
Reg. No. 35,477

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231, on June 24, 2002.

<u>Pat Slaback</u>
Name

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/086,129	02/28/2002	2133	908	X-1069 US	3	20	5

CONFIRMATION NO. 2232

24309
XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

FILING RECEIPT

OC000000007802144

Date Mailed: 04/05/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

David P. Schultz, San Jose, CA;

Assignment For Published Patent Application

Xilinx, Inc., San Jose, CA;

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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 04/04/2002

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

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INTELL. PROP. DEPT.
XILINX, INC.

Title

flexibly
Method and system for ~~flexible~~ nesting JTAG TAP controllers for FPGA-based system-on-chip (SoC)

Preliminary Class